

PATENT ABSTRACTS OF JAPAN

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(71)Applicant : FUJITSU LTD

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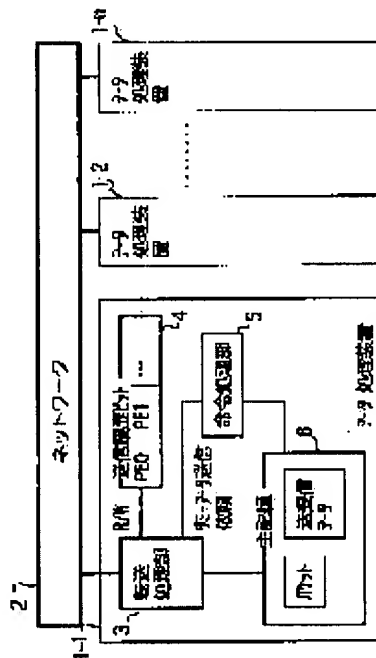
(72)Inventor : KUSANO YOSHIHIRO

(54) DATA PROCESSOR PROVIDED WITH TRANSMISSION HISTORY BIT AND DATA COMMUNICATION METHOD

(57)Abstract:

PROBLEM TO BE SOLVED: To improve execution performance of parallel system by reducing program execution overhead for judging completion of transmission.

SOLUTION: A transmission history bit 4 corresponding to a destination data processor or a destination virtual processor element is set whenever transferred data is transmitted to a network 2 by a transfer processing part 3. When transmission of dummy data is requested by an instruction processing part 5 following the data transmission, the dummy data to the data processor corresponding to the bit set by referring to the transmission history bit 4 or the virtual processor element is generated and the dummy data is transmitted to the data processor in which the transmission history bit is set by the transfer processing part 3.



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(74) 代理人 弁理士 長澤 俊一郎 (外1名)